



PSi TECHNOLOGIES ANNOUNCES APPOINTMENT OF JIM KNAPP
Industry Veteran Joins Company as Vice President of Technology, Package Development
and Central Engineering

South San Francisco, CA & Manila, Philippines – March 18, 2004 – PSi Technologies Holdings, Inc., (NASDAQ: PSIT), a leading independent provider of assembly and test services for the power semiconductor market, announced today the appointment of Jim Knapp as Vice President of Technology, Package Development and Central Engineering. Knapp is an accomplished technologist with over 30 years of development engineering and manufacturing management experience in the semiconductor industry at companies such as ON Semiconductor and Motorola. His technical experience spans the entire range of engineering, from package and product development through high volume manufacturing.

“Jim is an outstanding addition to PSi,” said Arthur J. Young, Jr., Chairman and CEO. “We expect that his extensive industry expertise, experience and technology leadership skills will be advantageous to the Company’s efforts in expanding its *Packaging Power Solutions™* product portfolio and services over the next year.” Among other initiatives, Knapp will be leading the PowerQFN™ (Quad Flat Pack Non-Lead) Packaging Platform development effort at PSi Technologies.

Knapp’s previous tenure at ON Semiconductor involved managing its Core Development Team responsible for the development of advanced semiconductor packages and manufacturing processes for ON’s power discrete, integrative passives and power components products. In addition to developing SMT materials for leadless surface mount, flip chip and chip scale packages, Knapp was one of the leaders behind the lead-free and green package development effort at ON. He also served as one of ON’s principal troubleshooters and provided lead technical support for its internal and external manufacturing centers and worldwide foundries in Europe and Asia.

At Motorola, Knapp served as a Packaging Development Manager in the Semiconductor Products Sector’s Advanced Package Design and Automation Center (APDAC). His 17-year package and product development tenure at Motorola spanned industries ranging from consumer electronics, automotive, computers, optoelectronics, telecommunications and defense. Knapp’s extensive experience with “Design For Manufacturability” has resulted in cost effective manufacturing processes and packages. Knapp served on Motorola’s technical advisory board and also performed troubleshooting for both its internal and external worldwide manufacturing centers.

Knapp led the engineering team responsible for the package design, development and pilot-line manufacturing of the world’s first commercially available bi-directional parallel fiber-optic link called OPTOBUS. The product involved an advanced optoelectronics MCM package that contained transceiver waveguides, laser diode arrays, photodetectors, transceiver drivers, and a fiber optic array interconnect. OPTOBUS was the forerunner of the Very Short Reach (VSR) transceivers used in today’s telecommunications industry. Knapp’s foray into the optoelectronics industry also included leading the package development of CCD and CMOS-based array imagers.

Knapp's innovative contributions to the industry include over 33 patents, with 11 pending as well as five publications. In addition, he has received Motorola's Global Achievement and Silver Quill Awards and Distinguished Innovator Award at both Motorola and ON Semiconductor. His work is well respected in the industry and his packaging accomplishments span areas such as conventional lead frame design, TAB, Flip chip, MCMs, BGAs, Molded Array Package Singulation (MAPS), plating and bump, transfer molding, QFPs and Power QFNs.

About PSi Technologies Holdings, Inc.

PSi Technologies is an independent semiconductor assembly and test service provider focused on the power semiconductor market. The Company provides comprehensive package design, assembly and test services for power semiconductors used in telecommunications and networking systems, computers and computer peripherals, consumer electronics, electronic office equipment, automotive systems and industrial products. PSi's customers include most of the major power semiconductor manufacturers in the world such as Fairchild Semiconductor, Infineon Technologies, ON Semiconductor, Philips Semiconductor, and ST Microelectronics.

For more information, visit the Company's web site at www.psitechnologies.com or call:

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Safe Harbor Statement

This press release contains forward-looking statements that involve risks and uncertainties. Actual outcomes may differ materially. Factors that might cause a difference include, but are not limited to, those relating to the pace of development and market acceptance of PSi's products and the power semiconductor market generally, commercialization and technological delays or difficulties, the impact of competitive products and technologies, competitive pricing pressures, manufacturing risks, the possibility of our products infringing patents and other intellectual property of third parties, product defects, costs of product development, manufacturing and government regulation, risks inherent in emerging markets, including but not limited to, currency volatility and depreciation, restricted access to financing, and political and social unrest. PSi undertakes no responsibility to update these forward-looking statements to reflect events or circumstances after the date hereof. More detailed information about potential factors that could affect PSi's financial results is included in the documents PSi files from time to time with the Securities and Exchange Commission.

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