



PSi TECHNOLOGIES TO PRESENT AT MEPTEC INTERCONNECTIONS INVESTORS CONFERENCE

South San Francisco, CA & Manila, Philippines – May 6, 2004 – PSi Technologies Holdings, Inc., (NASDAQ: PSIT), a leading independent provider of assembly and test services for the power semiconductor market, will present at the MicroElectronics Packaging and Test Engineering Council (MEPTEC) Interconnections Investors Conference in Santa Clara, California on Wednesday, May 12 at 1:20 p.m. Pacific. A group breakout session with investors is scheduled for 3:15 p.m. Pacific Time. Chief Financial Officer Thelma G. Oribello and Vice President for Technology, Package Development & Central Engineering Jim Knapp will present and meet with investors at the event.

About PSi Technologies

PSi Technologies is a focused independent semiconductor assembly and test service provider to the power semiconductor market. The Company provides comprehensive package design, assembly and test services for power semiconductors used in telecommunications and networking systems, computers and computer peripherals, consumer electronics, electronic office equipment, automotive systems and industrial products. Their customers include most of the major power semiconductor manufacturers in the world such as Fairchild Semiconductor, Infineon Technologies, ON Semiconductor, Philips Semiconductor, and ST Microelectronics. For more information, visit the Company's web site at www.psitechnologies.com or call:

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